

DATA SHEET

MODULETEK:QSFP28-SR4-C10

100G QSFP28 SR4 Optical Transceiver

QSFP28-SR4-C10 Overview

ModuleTek's QSFP28-SR4-C10 QSFP28 SR4 optical transceivers are based on 100G Ethernet IEEE 802.3bm standard. QSFP28 SR4 offers 4 independent transmit and receive channels, each capable of 25G for an aggregate bandwidth of 100G.

Product Features

- Hot-pluggable QSFP28 form factor
- 4x25Gbps 850nm VCSEL laser
- Up to 103.1Gbps
- QSFP28 MSA compliant
- Up to 70m on OM3 Multimode Fiber and 100m on OM4 Multimode Fiber
- Single 1X12 MPO receptacle
- RoHS Compliant
- Operating temperature range: 0°C to70°C

Applications

- 100GBASE-SR4 100G Ethernet

Ordering Information

Part Number	Description	Color on Clasp
QSFP28-SR4-C10	100G QSFP28 850nm MPO Connectors, Up to 70m(OM3) or 100m(OM4) on MMF	cream color
For More Information: ModuleTek Limited Web: www.moduletek.com Email: sales@moduletek.com		

General Specifications

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Bit Error Rate	BER			10^{-12}		
Operating Temperature	T _C	0		70	°C	1
Storage Temperature	T _{STO}	-40		85	°C	2
Input Voltage	V _{CC}	3.14	3.3	3.46	V	
Maximum Voltage	V _{MAX}	-0.5		4	V	3

Notes:

1. Case temperature
2. Ambient temperature
3. For electrical power interface

Optical – Characteristics – Transmitter

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Average Launch Power (per lane)	P	-8.4		2.4	dBm	
Optical Center Wavelength	λ_C	840		860	nm	
Extinction Ratio	ER	2			dB	
RMS Spectral Width	$\Delta\lambda$			0.65	nm	
Relative Intensity Noise	RIN			-12.8	dB/Hz	
Launch Power of OFF Transmitter	P _{OUT_OFF}			-30	dBm	1
Transmitter Eye Mask		Compliant with IEEE 802.3bm				

Notes:

1. Average

Optical – Characteristics – Receiver

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Optical Center Wavelength	λ_C	840		860	nm	
Average Receive Power (per lane)	P _{RX}	-10.3		2.4	dBm	
Damage Threshold	P	3.4			dBm	
Receiver Sensitivity (per lane)	R _{X_SEN}			-10.3	dBm	
Receiver Reflectance	TR _{RX}			-12	dB	
LOS Assert	LOS _A	-30			dBm	
LOS De-Assert	LOS _D			-13	dBm	
LOS Hysteresis	LOS _H	0.5			dB	

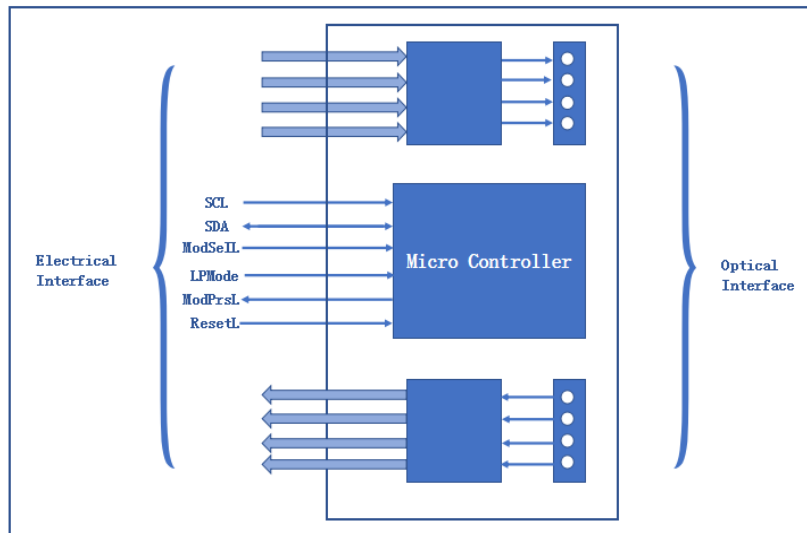
Electrical- Characteristics - Transmitter

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Data Rate Per Channel	DR		25.78125		Gb/s	
Differential data input swing	V_{IN_PP}	180		1200	mV	
Transmit disable voltage	V_D	$V_{CC}-1.3$		V_{CC}	V	
Transmit enable voltage	V_{EN}	V_{EE}		$V_{EE}+0.8$	V	

Electrical- Characteristics - Receiver

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Data Rate Per Channel	DR		25.78125		Gb/s	
Differential Data Output Swing	V_{OUT_PP}	400		800	mV	
Single Ended Voltage Tolerance	V	-0.35		3.3	V	
Data Output Rise Time (20%-80%)	t_r	12			ps	
Data Output Fall Time (20%-80%)	t_f	12			ps	

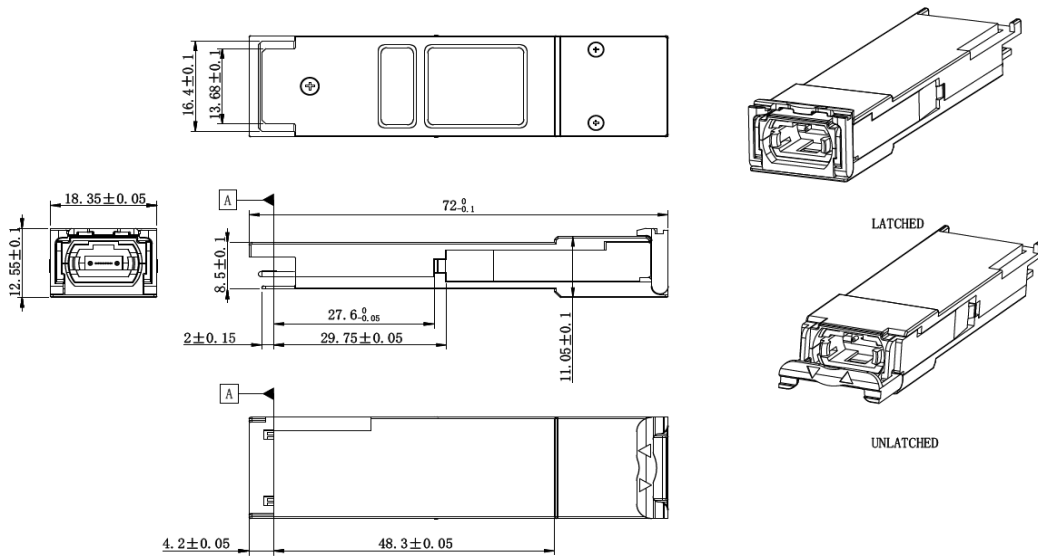
Block-Diagram-of-Transceiver



Functions Description

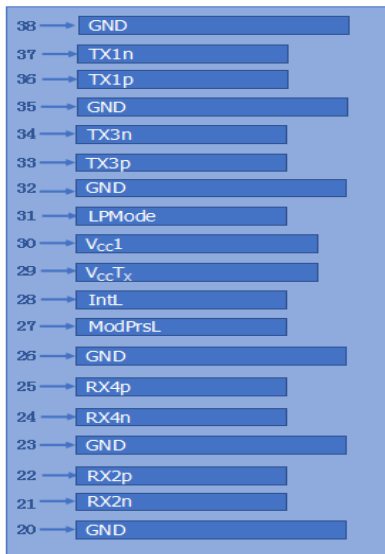
The QSFP28-SR4 has miniature optical engine embedded into QSFP28 module. The engines interconnect 4 independent transmit/receive lanes. A functional block diagram of the engine is shown in the above Figure. The transmitter sections consist of a 4-channel VCSEL array, a 4-channel input buffer and laser driver. An on board micro-controller provides control, diagnostic and monitoring for the cable functions, as well as the external I2C serial communication interface. The Receiver section consists of a 4-channel PIN photodiode array, a 4-channel TIA array, and a 4-channel output buffer.

Dimensions

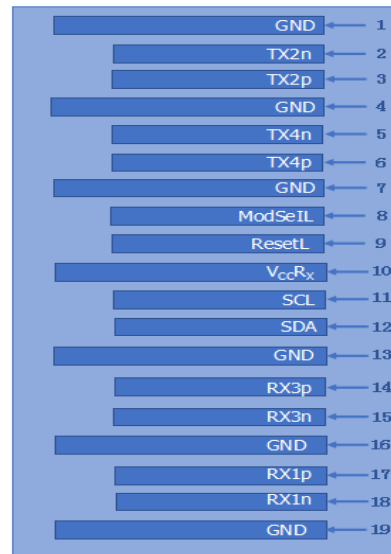


ALL DIMENSIONS ARE ± 0.2 mm UNLESS OTHERWISE SPECIFIED
UNIT: mm

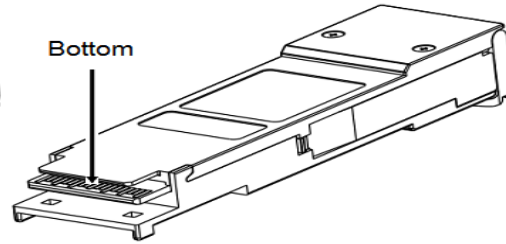
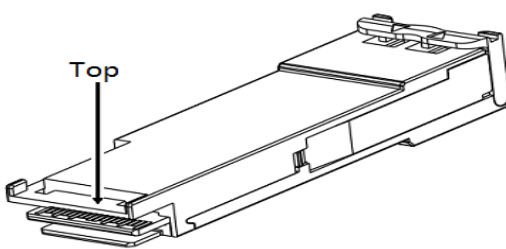
Electrical Pad Layout



Top of Board



Bottom of Board



Pin Assignment

PIN #	Symbol	Description	Remarks
1	GND	Ground	5
2	Tx2n	Transmitter Inverted Data Input	
3	Tx2p	Transmitter Non-Inverted Data Input	
4	GND	Ground	5
5	Tx4n	Transmitter Inverted Data Input	
6	Tx4p	Transmitter Non-Inverted Data Input	
7	GND	Ground	5
8	ModSelL	Module Insertion Indicator Pin	1
9	ResetL	Module Reset	2
10	V _{cc} R _X	+3.3V Power Supply Receiver	
11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	5
14	Rx3p	Receiver Non-Inverted Data Output	
15	Rx3n	Receiver Inverted Data Output	
16	GND	Ground	5
17	Rx1p	Receiver Non-Inverted Data Output	
18	Rx1n	Receiver Inverted Data Output	
19	GND	Ground	5
20	GND	Ground	5
21	Rx2n	Receiver Inverted Data Output	
22	Rx2p	Receiver Non-Inverted Data Output	
23	GND	Ground	5
24	Rx4n	Receiver Inverted Data Output	
25	Rx4p	Receiver Non-Inverted Data Output	
26	GND	Ground	5
27	ModPrsL	Module Present	3
28	IntL	Interrupt	4
29	V _{cc} T _X	+3.3V Power Supply transmitter	
30	V _{cc} 1	+3.3V Power Supply	
31	LPMMode	Low Power Mode	5
32	GND	Ground	5
33	Tx3p	Transmitter Non-Inverted Data Input	

34	Tx3n	Transmitter Inverted Data Input	
35	GND	Ground	5
36	Tx1p	Transmitter Non-Inverted Data Input	
37	Tx1n	Transmitter Inverted Data Input	
38	GND	Ground	5

Notes:

1. ModSelL is the input pin. The module responds to 2-wire serial communication commands when it is held low by the host. ModSelL allows multiple QSFP modules to be used on a single 2-wire interface bus. If ModSelL is High, the module will not respond to any 2-wire interface communication from the host. ModSelL has internal pull-up resistors in the module
2. The module restart pin, when the low level on the ResetL pin lasts longer than the minimum pulse length, resets the module and restores all user modules to their default state. When performing reset device, the host should ignore all status bits. Until the module reset interrupt is completed, please note that during hot plugging, the module will issue this information to complete the reset interrupt without resetting
3. This pin is active high, indicating that the module is running under a low power module.
4. IntL is the output pin, which is the open collector output and must be pulled up to Vcc on the motherboard. When it is low, it indicates that the module may malfunction. The host uses a 2-wire serial interface to identify the interrupt source
5. Circuit ground is internally isolated from chassis ground.

References

1. IEEE standard 802.3ba. IEEE Standard Department.
2. QSFP28 4X PLUGGABLE TRANSCEIVER –SFF-8665