

DATA SHEET

MODULETEK: XFP-10G-DWDM-ZR-xx.xx-x-C10

10GBASE DWDM XFP Optical Transceiver

XFP-10G-DWDM-ZR-xx.xx-x-C10 Overview

ModuleTek's XFP-10G-DWDM-ZR-xx.xx-x-C10 10 Gb/s DWDM XFP optical transceivers are designed for Storage, IP network and SONET/SDH, it supports DWDM 10G Ethernet, DWDM 10G Fiber channel and DWDM 10G SONET/SDH interface. The product is a hot pluggable module in the Z-direction, mainly used for typical router/switch line card applications. The product implements digital diagnostics functions via a 2-wire serial interface, compliant with XFP Multi-Source Agreement (MSA) Specification.

Product Features

- Up to 9.95 Gb/s to 11.3 Gb/s bit rates
- Compliant with 10GEthernet, 10GFC, OC192 application
- Compliant with XFP MSA
- Standard bail mechanism for consistent installation and removal
- Temperature-stabilized DWDM EML laser
- Receiver with APD
- 100GHz ITU Grid, C Band
- Built-in digital diagnostic functions
- Hot Pluggable XFP footprint
- Duplex LC Connectors
- Up to 80km on SMF
- Low power consumption <3W
- RoHS Compliant
- Operating temperature range (Case Temperature)
 - Commercial Level : 0°C to 70°C
 - Industrial Level : -40°C to 85°C



Applications

- DWDM 10G Ethernet
- DWDM 10G Fiber Channel
- DWDM OC192 /STM-64

Ordering Information

Part Number	Product ID	Description	Color on Clasp
XFP-10G-DWDM-ZR-xx.xx-C-C10	M362300	10GBASE DWDM XFP Transceiver, Single Mode Fiber 80km (ITU 100GHz Grid), Commercial temperature	Cold gray
XFP-10G-DWDM-ZR-xx.xx-I-C10	M391200	10GBASE DWDM XFP Transceiver, Single Mode Fiber 80km (ITU 100GHz Grid), Industrial temperature	Cold gray
Note: 1.Product ID is the abbreviated order number of our company's standard model			

For More Information:

ModuleTek Limited

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Email : sales@moduletek.com

Product Selection

Product number	Description	ITU channel
XFP-10G-DWDM-ZR-63.05-x-C10	10GBASE-DWDM 1563.05 nm XFP	C18
XFP-10G-DWDM-ZR-62.23-x-C10	10GBASE-DWDM 1562.23 nm XFP	C19
XFP-10G-DWDM-ZR-61.42-x-C10	10GBASE-DWDM 1561.42 nm XFP	C20
XFP-10G-DWDM-ZR-60.61-x-C10	10GBASE-DWDM 1560.61 nm XFP	C21
XFP-10G-DWDM-ZR-59.79-x-C10	10GBASE-DWDM 1559.79 nm XFP	C22
XFP-10G-DWDM-ZR-58.98-x-C10	10GBASE-DWDM 1558.98 nm XFP	C23
XFP-10G-DWDM-ZR-58.17-x-C10	10GBASE-DWDM 1558.17 nm XFP	C24
XFP-10G-DWDM-ZR-57.36-x-C10	10GBASE-DWDM 1557.36 nm XFP	C25
XFP-10G-DWDM-ZR-56.55-x-C10	10GBASE-DWDM 1556.55 nm XFP	C26
XFP-10G-DWDM-ZR-55.75-x-C10	10GBASE-DWDM 1555.75 nm XFP	C27
XFP-10G-DWDM-ZR-54.94-x-C10	10GBASE-DWDM 1554.94 nm XFP	C28
XFP-10G-DWDM-ZR-54.13-x-C10	10GBASE-DWDM 1554.13 nm XFP	C29
XFP-10G-DWDM-ZR-53.33-x-C10	10GBASE-DWDM 1553.33 nm XFP	C30
XFP-10G-DWDM-ZR-52.52-x-C10	10GBASE-DWDM 1552.52 nm XFP	C31
XFP-10G-DWDM-ZR-51.72-x-C10	10GBASE-DWDM 1551.72 nm XFP	C32
XFP-10G-DWDM-ZR-50.92-x-C10	10GBASE-DWDM 1550.92 nm XFP	C33
XFP-10G-DWDM-ZR-50.12-x-C10	10GBASE-DWDM 1550.12 nm XFP	C34
XFP-10G-DWDM-ZR-49.32-x-C10	10GBASE-DWDM 1549.32 nm XFP	C35
XFP-10G-DWDM-ZR-48.51-x-C10	10GBASE-DWDM 1548.51 nm XFP	C36

XFP-10G-DWDM-ZR-47.72-x-C10	10GBASE-DWDM 1547.72 nm XFP	C37
XFP-10G-DWDM-ZR-46.92-x-C10	10GBASE-DWDM 1546.92 nm XFP	C38
XFP-10G-DWDM-ZR-46.12-x-C10	10GBASE-DWDM 1546.12 nm XFP	C39
XFP-10G-DWDM-ZR-45.32-x-C10	10GBASE-DWDM 1545.32 nm XFP	C40
XFP-10G-DWDM-ZR-44.53-x-C10	10GBASE-DWDM 1544.53 nm XFP	C41
XFP-10G-DWDM-ZR-43.73-x-C10	10GBASE-DWDM 1543.73 nm XFP	C42
XFP-10G-DWDM-ZR-42.94-x-C10	10GBASE-DWDM 1542.94 nm XFP	C43
XFP-10G-DWDM-ZR-42.14-x-C10	10GBASE-DWDM 1542.14 nm XFP	C44
XFP-10G-DWDM-ZR-41.35-x-C10	10GBASE-DWDM 1541.35 nm XFP	C45
XFP-10G-DWDM-ZR-40.56-x-C10	10GBASE-DWDM 1540.56 nm XFP	C46
XFP-10G-DWDM-ZR-39.77-x-C10	10GBASE-DWDM 1539.77 nm XFP	C47
XFP-10G-DWDM-ZR-38.98-x-C10	10GBASE-DWDM 1538.98 nm XFP	C48
XFP-10G-DWDM-ZR-38.19-x-C10	10GBASE-DWDM 1538.19 nm XFP	C49
XFP-10G-DWDM-ZR-37.40-x-C10	10GBASE-DWDM 1537.40 nm XFP	C50
XFP-10G-DWDM-ZR-36.61-x-C10	10GBASE-DWDM 1536.61 nm XFP	C51
XFP-10G-DWDM-ZR-35.82-x-C10	10GBASE-DWDM 1535.82 nm XFP	C52
XFP-10G-DWDM-ZR-35.04-x-C10	10GBASE-DWDM 1535.04 nm XFP	C53
XFP-10G-DWDM-ZR-34.25-x-C10	10GBASE-DWDM 1534.25 nm XFP	C54
XFP-10G-DWDM-ZR-33.47-x-C10	10GBASE-DWDM 1533.47 nm XFP	C55
XFP-10G-DWDM-ZR-32.68-x-C10	10GBASE-DWDM 1532.68 nm XFP	C56
XFP-10G-DWDM-ZR-31.90-x-C10	10GBASE-DWDM 1531.90 nm XFP	C57
XFP-10G-DWDM-ZR-31.12-x-C10	10GBASE-DWDM 1531.12 nm XFP	C58
XFP-10G-DWDM-ZR-30.33-x-C10	10GBASE-DWDM 1530.33 nm XFP	C59
XFP-10G-DWDM-ZR-29.55-x-C10	10GBASE-DWDM 1529.55 nm XFP	C60
XFP-10G-DWDM-ZR-28.77-x-C10	10GBASE-DWDM 1528.77 nm XFP	C61

Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Storage Ambient Temperature	T _S	-40		85	°C	
Supply Voltage 5V	V _{CC5}	-0.5		5.5		
Supply Voltage 3.3V	V _{CC3}	-0.5		4	V	

General Specifications

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Data Rate	DR	9.95		11.3	Gb/s	
Bit Error Rate	BER			10^{-12}		
Total Power Consumption	P			3	W	
Supply Voltage -5V	V _{CC5}	4.75		5.25	V	1
Supply Voltage -3.3V	V _{CC3}	3.14		3.46	V	1
Supply Current -V _{CC5} supply	I _{CC5}			350		
Supply Current -V _{CC3} supply	I _{CC3}			500	mA	
Operating Temperature	T _C	0		70	°C	2
	T _I	-40		85	°C	2

Notes:

1. Operating Environment
2. Case temperature

Link Distances

Data Rate	Fiber Type	Distance Range (km)
9.95 – 11.3 Gb/s	9/125um SMF	80

Optical – Characteristics – Transmitter

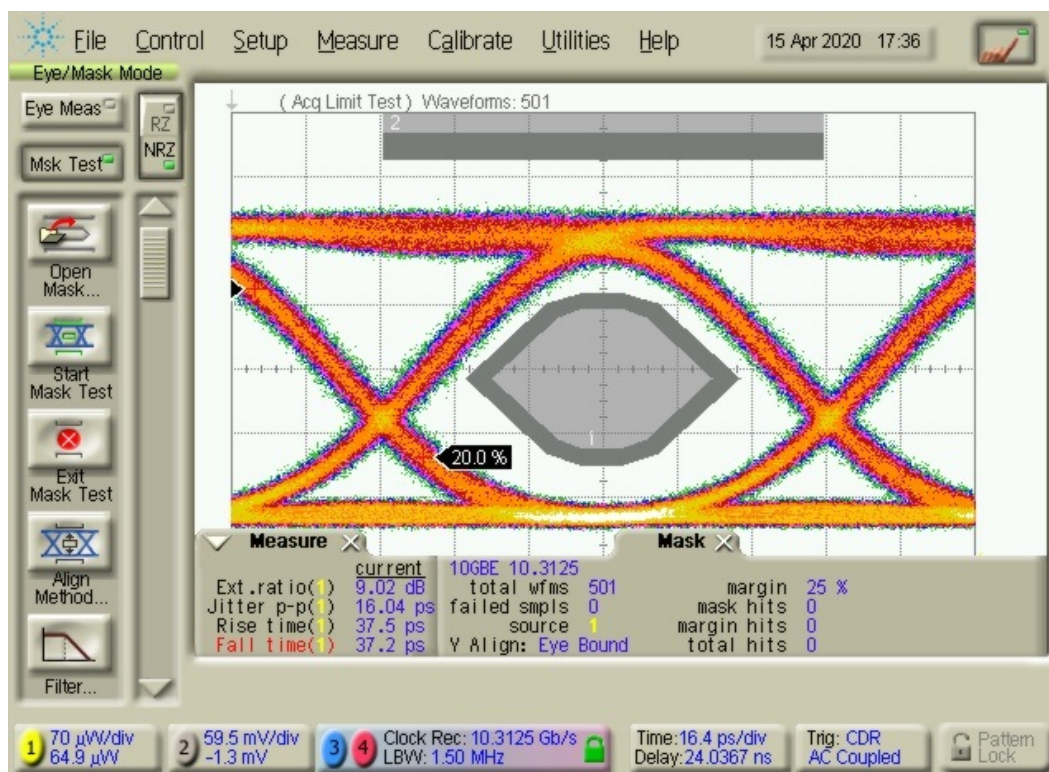
$V_{CC5}=4.75V$ to $5.25V$, $V_{CC3}=3.14V$ to $3.46V$, T_C

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Optical Center Wavelength	λ_C	$\lambda_C-0.1$	λ_C	$\lambda_C+0.1$	nm	1
Output Optical Power	P_{TX}	0		4	dBm	2
Extinction Ratio	ER	9			dB	
Side Mode Suppression Ratio	SMSR	30			dB	
Relative Intensity Noise	RIN			-130	dB/Hz	
Transmitter Dispersion Penalty	TDP			3	dB	
Launch Power of OFF Transmitter	P_{OUT_OFF}			-30	dBm	2
Transmitter Jitter (Peak-to-Peak)	TJ			0.1	UI	

Notes:

1. λ =specified ITU Grid wavelength
2. Average

Typical eye diagram



Optical – Characteristics – Receiver

$V_{CC5}=4.75V$ to $5.25V$, $V_{CC3}=3.14V$ to $3.46V$, T_C

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Center Wavelength Range	λ_C	1260		1600	nm	
Receiver Overload	P_{OL}	-7			dBm	
Receiver Sensitivity @10.3Gb/s	R_{X_SEN1}			-24	dBm	1
Receiver Reflectance	TR_{RX}			-27	dB	
LOS Assert	LOS_A	-35			dBm	
LOS De-Assert	LOS_D			-27	dBm	
LOS Hysteresis	LOS_H	0.5			dB	

Notes:

1. Measured with worst ER: $BER < 10^{-12}$; $2^{31}-1$ PRBS

Electrical – Characteristics – Transmitter

$V_{CC5}=4.75V$ to $5.25V$, $V_{CC3}=3.14V$ to $3.46V$, T_C

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Input differential impedance	R_{IN}		100		Ω	1
Differential data input swing	V_{IN_PP}	120		820	mV	
Transmit Disable Voltage	V_D	2		V_{CC}	V	2
Transmit Enable Voltage	V_{EN}	GND		GND+0.8	V	
Transmit Disable Assert Time				10	us	

Notes:

1. After internal AC coupling
2. Or open circuit

Electrical – Characteristics – Receiver

$V_{CC5}=4.75V$ to $5.25V$, $V_{CC3}=3.14V$ to $3.46V$, T_C

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Differential data output swing	V_{OUT_PP}	340	650	850	mV	
Data output rise time/fall time (20%-80%)	t_r/t_f			38	ps	
LOS Fault	V_{LOS_A}	$V_{CC}-0.5$		V_{CC_HOST}	V	
LOS Normal	V_{LOS_D}	GND		GND+0.5	V	

A0 Write Protection

Security Level 1 Password		
Password Entry ADDR	Size	Vaules(hex)
Page A0, 7BH-7EH	4	00 00 10 11

This module has the A0 write protection function. The user can enter the security level 1 working state and write the contents of Table 00 and Table 01 of the device address A0H of the module. The method to enter the working state of security level 1 is to write the security level 1 password in order in the 7BH-7EH registers of A0H of the module. After entering security level 1, the user can directly write to the contents of the A0H device address, or modify the contents of the A0H 7F table selection register to write to the contents of Table 00 or Table 01. This version of the module does not support users to modify the password of security level 1. If you need to modify the security level 1 password, you must notify our company to modify it before shipping.

I2C Memory Map (Page A0 HEX,Unlisted Fields are Blank/Empty)

IIC ADDR	Size	Name	Description	Vaules(HEX)
128	1	Identifier	XFP	06
129	1	Ext. Identifier	1.Power Level 3 Module (3.5W max. power dissipation.) 2.TX Ref Clock Input Not Required	90
130	1	Connector	Connector Type=LC	07
131-138	8	Transceiver	SONET/SDH Codes –Long Haul:G.959.1 P1L1-2D2	00 00 00 00 00 00 04 00
139	1	Encoding	Code for serial encoding algorithm	F0
140	1	BR-Min	Minimum bit rate, units of 100 MBits/s.	63
141	1	BR-Max	Maximum bit rate, units of 100 MBits/s.	6B
142	1	Length(SMF)-km	80km Link Length in Thousands of Meters / SMF	50
143	1	Length (E-50µm)	Link length supported for EBW 50/125 µm fiber, units of 2 m	00
144	1	Length (50 µm)	Link length supported for 50/125 µm fiber, units of 1 m	00
145	1	Length (62.5 µm)	Link length supported for 62.5/125 µm fiber, units of 1 m	00
146	1	Length(Copper)	Link length supported for copper, units of 1m	00
147	1	Device Tech	1.1550 nm EML 2.Cooled transmitter 3.APD detector	76
148-163	16	Vendor name	ModuleTek	ASCII Format
164	1	CDR Support	CDR Rate Support	F9
165-167	3	Vendor OUI	XFP vendor IEEE company ID	00 00 00
168-183	16	Vendor PN	Part number provided by XFP vendor (ASCII)	ASCII Format
184-185	2	Vendor Revision Number	Revision level for part number provided by vendor (ASCII)	Programmed by Factory
186-187	2	Wavelength	Nominal laser wavelength (Wavelength = value / 20 in nm)	according to actual wavelength
188-189	1	Wavelength Tolerance	Guaranteed range of laser wavelength (+/- value) from Nominal wavelength. (Wavelength Tol. = value/200 in nm)	00 14
190	1	Max Case Temp	Maximum Case Temperature in Degrees C.	46

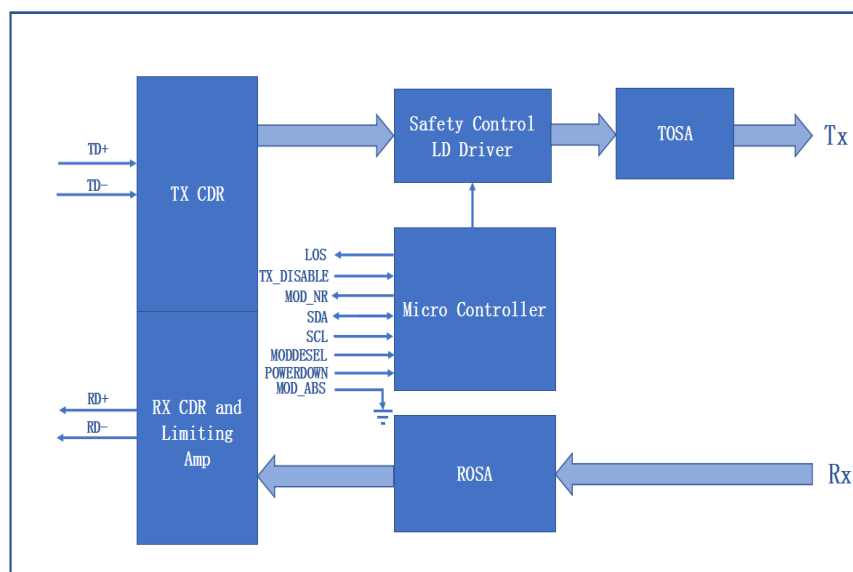
191	1	CC_BASE	Check code for Base ID Fields (addresses 120-190)	Programmed by Factory
192-195	4	Power Supply	Power supply current requirements and max power dissipation	00
196-211	16	Vendor SN	Serial number provided by vendor (ASCII)	00
212-219	8	Date code	Vendor's manufacturing date code	00
220	1	Diagnostic Monitoring Type	Received power measurement type-Average Power	08
221	8	Enhanced Options	1. Optional Soft TX_DISABLE implemented 2.Optional Soft P_down implemented	60
222	1	Aux Monitoring	Auxiliary Input Types: 1.Laser Temperature 2.+5V Supply Voltage	46
223	1	CC_EXT	Check sum of bytes 192-222	F0
224-255	32	Vendor Specific	Vendor Specific	Programmed by Factory

Digital Diagnostic Functions

ModuleTek's XFP-10G-DWDM-ZR-xx.xx-x-C10 incorporates a XFP compliant 2-wire management interface which is used for serial ID, digital diagnostics and certain control functions. It is modeled on the SFF-8472 Rev 9.3 specification modified to accommodate a single 2-wire interface address. In addition to the basic I2C read/write functionality the modules support packet error checking that, when enabled, allows the host system to confirm the validity of any read data. Details of the protocol and interface are explicitly described in the MSA. The digital diagnostics provides real-time access to operating parameters via a 2-wire serial interface, including operating temperature, laser bias current, transmitted optical power, received optical power and operating voltage.

Digital Diagnostic Threshold Range					
Parameter		High Alarm(hex)	High Warning(hex)	Low Warning(hex)	Low Alarm(hex)
Temperature(°C)	T _C	75 (0x4B00)	70 (0x4600)	0 (0x0000)	-5 (0xFB00)
	T _I	90 (0x5A00)	85 (0x5500)	-40 (0xD800)	-45 (0xD300)
5V Voltage(V)		5.5 (0xD6D8)	5.25 (0xCD14)	4.75 (0xB98C)	4.5 (0xAFC8)
3.3V Voltage(V)		3.63 (0x8DCC)	3.46 (0x8728)	3.13 (0x7A44)	2.97 (0x7404)
1.8V Voltage(V)		1.98 (0x4D58)	1.89 (0x49D4)	1.71 (0x42CC)	1.62 (0x3F48)
Bias Current(mA)		131 (0xFFDC)	130 (0xFDE8)	20 (0x2710)	15 (0x1D4C)
Tx Power(μW)		3014.2 (0x75BE)	2512 (0x621E)	1000 (0x2710)	800 (0x1F40)
Rx Power(μW)		251.7 (0x09D5)	188.8 (0x0760)	2.9 (0x001D)	1.9 (0x0013)

Block-Diagram-of-Transceiver



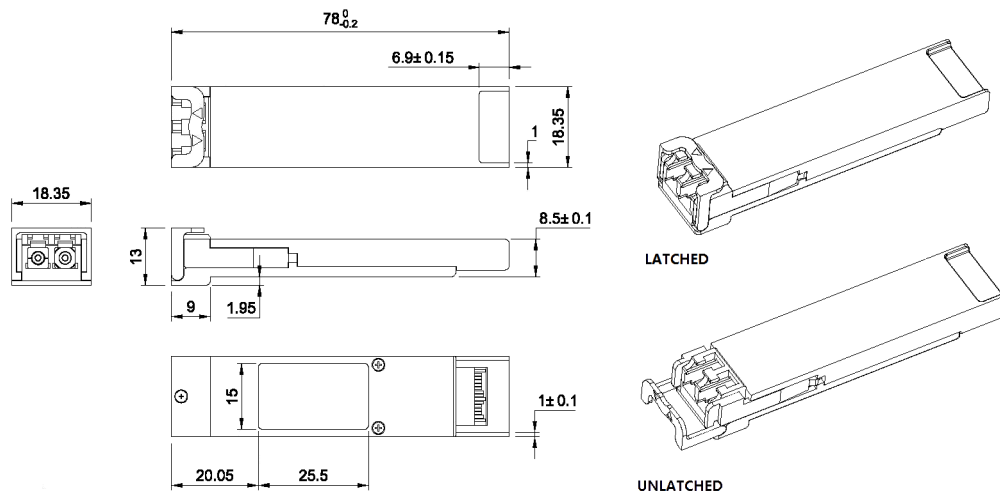
Functions Description

The transmitter consists of a laser driver chip and a TOSA (light-emitting component). The TOSA includes a laser, an electroabsorption modulator (EAM), a TEC, and a backlight diode. Unlike DML, EML TOSA uses external modulation. When the transmission rate of the system is high, the transmission distance is not only limited by the attenuation of the optical fiber, but also by the dispersion of the optical fiber and the dispersion of the optical communication system is related to the modulation spectral width. The directly modulated laser has a large dispersion cost, and has spectral broadening, frequency chirp, and short transmission distance, so the external modulation method is adopted in the medium-long-distance transmission at a rate of 10 Gb/s. The TEC (Thermo Electric Cooler) Controls the temperature of the laser tube. When a temperature is set, the TOSA temperature of the module will remain unchanged through the control of the TEC. The wavelength of the module laser tube is related to the temperature of the laser tube. The module wavelength can be set by setting the TOSA temperature. When stable, the module has excellent wavelength stability. The electrical signal enters the optical module from the serial electrical interface and is then input to the laser driver chip. The laser driver chip supplies the bias current and the modulation current to the laser. The laser driver chip simultaneously uses an automatic optical power control (APC) feedback loop to maintain a constant average optical power of the laser output. The purpose is to eliminate the change of the output optical signal due to temperature changes and aging of the light source device. Clock Data Recovery Module (CDR) overcomes board and connector degradation by reshaping, regenerating, and reducing jitter. TX_DIS is an input pin. When TX_DIS is asserted High, the XFP module transmitter output must be turned off. The receiver consists of a ROSA (light-receiving component) and a limiting amplifier chip, ROSA includes a APD photodetector and a transimpedance amplifier chip. The ROSA detects the incident optical signal, converts the optical signal into an electrical signal, and outputs the electrical signal to the limiting amplifier. The limiting amplifier further amplifies the input signal and outputs a fixed amplitude electrical signal, then shapes and synchronizes the electrical signal to the host through the CDR. Clock Data Recovery Module (CDR) overcomes board and connector degradation by reshaping, regenerating, and reducing jitter. The RX_LOS of an output pin, when LOS is high, it indicates insufficient optical power for reliable signal reception. MOD_NR is an output pin. When it is high, it indicates that the module detects the condition that invalidates the transmitter/receiver data. The transmission signal regulator loses lock, the transmitter laser fails, and the receiver signal regulator loses lock. The micro controller unit initializes the control register of laser driver, limiting amplifier and CDR. It monitors the running information from the laser driver, limiting amplifier and CDR, then reports this information to the customer.

Dimensions

Module Weight: 39g

Dust Cap Weight: 0.95g



ALL DIMENSIONS ARE ± 0.2 mm UNLESS OTHERWISE SPECIFIED
UNIT: mm

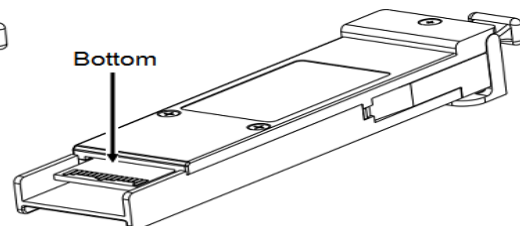
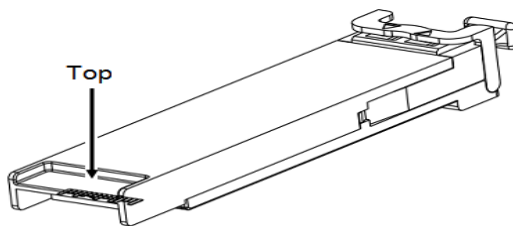
Electrical Pad Layout

30	→	GND
29	→	TD+
28	→	TD-
27	→	GND
26	→	GND
25	→	REFCLK-
24	→	REFCLK+
23	→	GND
22	→	VCC2
21	→	P_DOWN/RST
20	→	VCC2
19	→	GND
18	→	RD+
17	→	RD-
16	→	GND

Top of Board

1	→	GND
2	→	VEE5
3	→	MOD_DESEL
4	→	$\overline{\text{INTERRUPT}}$
5	→	TX_DIS
6	→	VCC5
7	→	GND
8	→	VCC3
9	→	VCC3
10	→	SCL
11	→	SDA
12	→	MOD_ABS
13	→	MOD_NR
14	→	RX_LOS
15	→	GND

Bottom of Board



Pin Assignment - Pin1 to Pin30

PIN #	Symbol	Description	Remarks
1	GND	Module Ground	1
2	VEE5	Optional –5.2 Power Supply (Not required)	
3	Mod-Desel	Module De-select, when held low allows the module to respond to 2-wire serial interface commands	
4	Interrupt	Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	TX_DISABLE	Transmitter Disable, Transmitter laser source turned off	
6	VCC5	+5V Power Supply	
7	GND	Module Ground	1
8	VCC3	+3.3V Power Supply	
9	VCC3	+3.3V Power Supply	
10	SCL	Serial 2-wire interface clock	2
11	SDA	Serial 2-wire interface data line	2
12	Mod_ABS	Module Absent; Indicates module is not present. Grounded in the module	2
13	Mod_NR	Module Not Ready; Indicates module operating fault	2
14	RX_LOS	Receiver Loss of Signal indicator	2
15	GND	Module Ground	1
16	GND	Module Ground	1
17	RD-	Receiver inverted data output	
18	RD+	Receiver non-inverted data output	
19	GND	Module Ground	1
20	VCC2	+1.8V Power Supply	
21	P_Down/RST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module rest	
		Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle	
22	VCC2	+1.8V Power Supply	
23	GND	Module Ground	1
24	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board	
25	RefCLK-	Reference Clock inverted input, AC coupled on the host board	
26	GND	Module Ground	1
27	GND	Module Ground	1

28	TD-	Transmitter inverted data input	
29	TD+	Transmitter non-inverted data input	
30	GND	Module Ground	1

Notes:

- 1.Module ground pins (GND) are isolated from the module case and chassis ground within the module
- 2.Open collector, should be pulled up with 4.7k Ω -10k Ω on host board to a voltage between 3.15V and 3.6V

References

- 1.10GBASE Small Form Factor Pluggable Module(XFP)Multi-Source Agreement(MSA),Rev4.5 – August 2005.